

FIG. 1

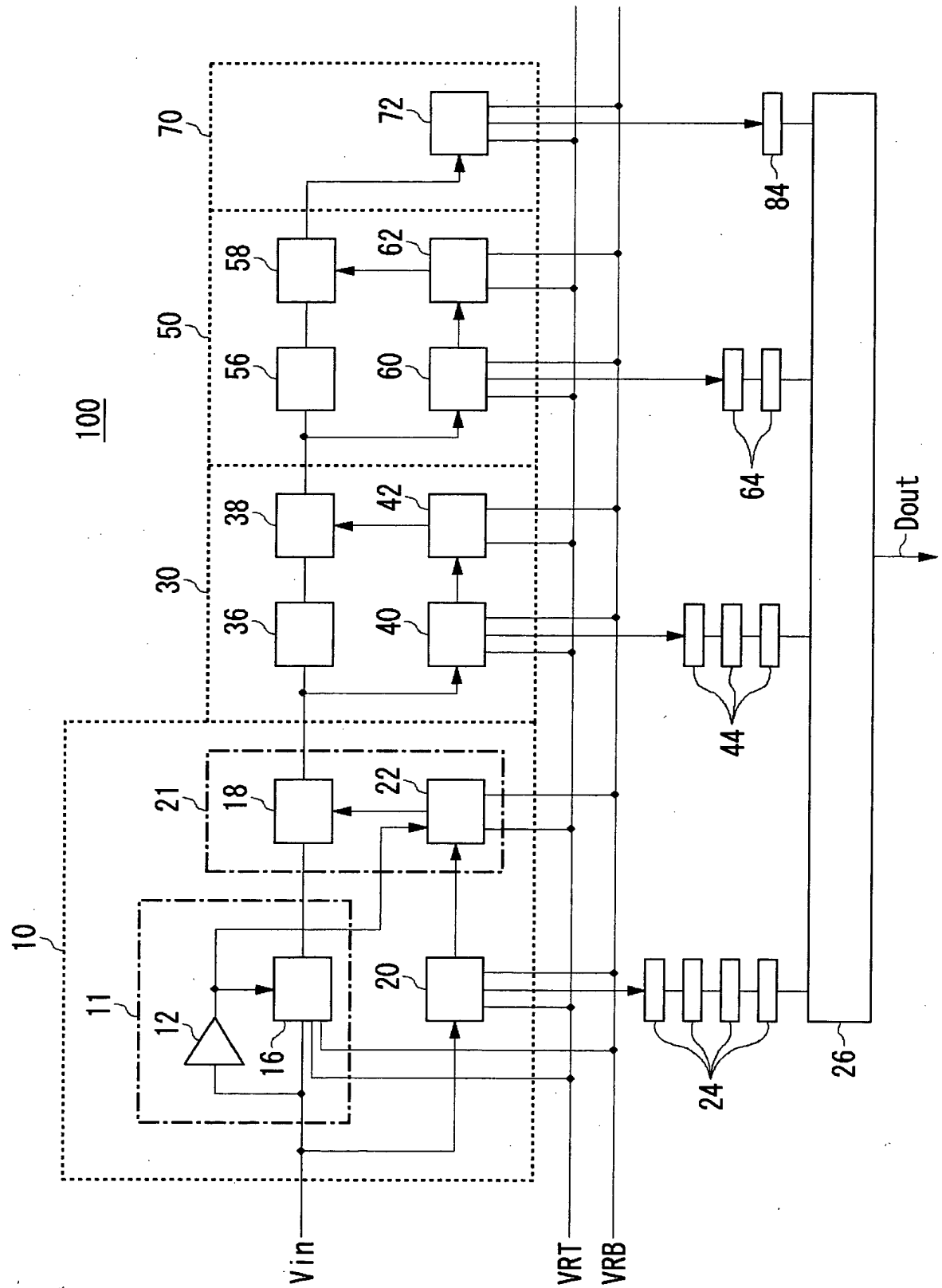


FIG. 2

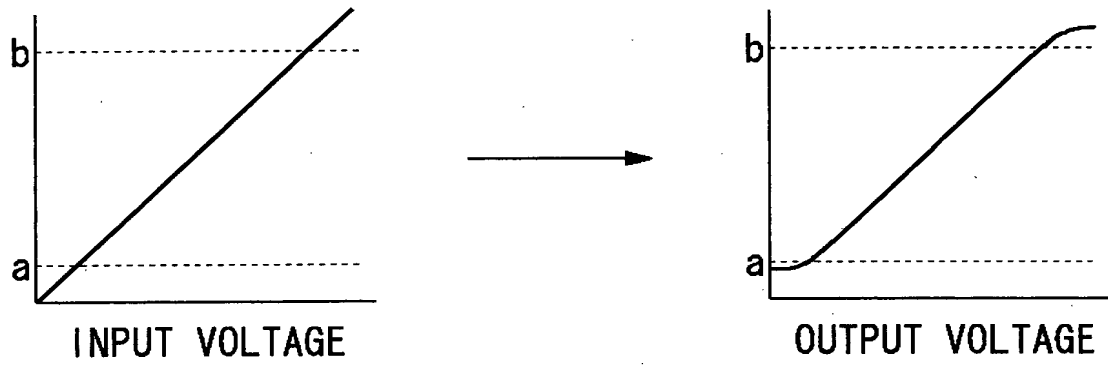


FIG. 3

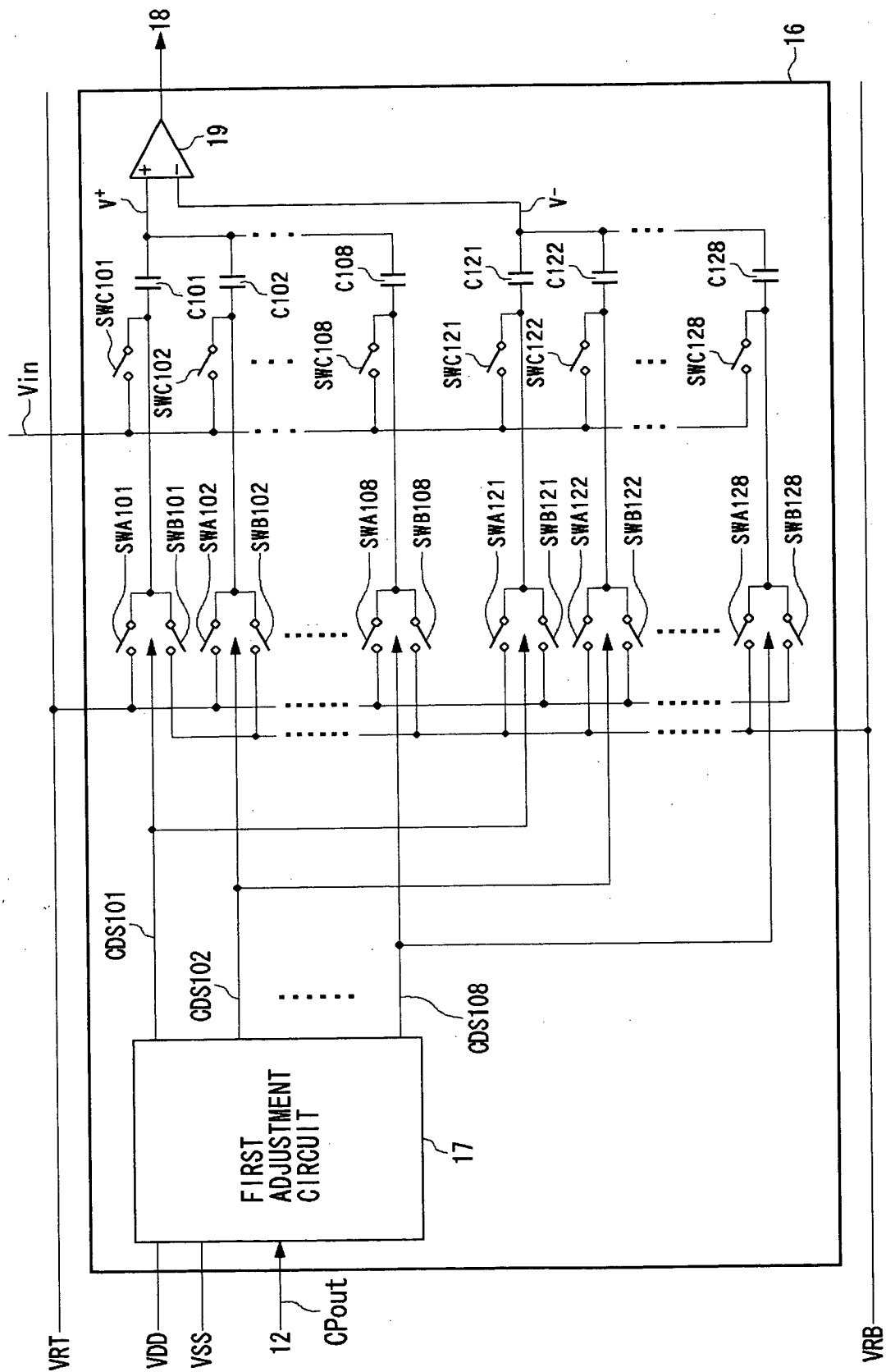


FIG. 4

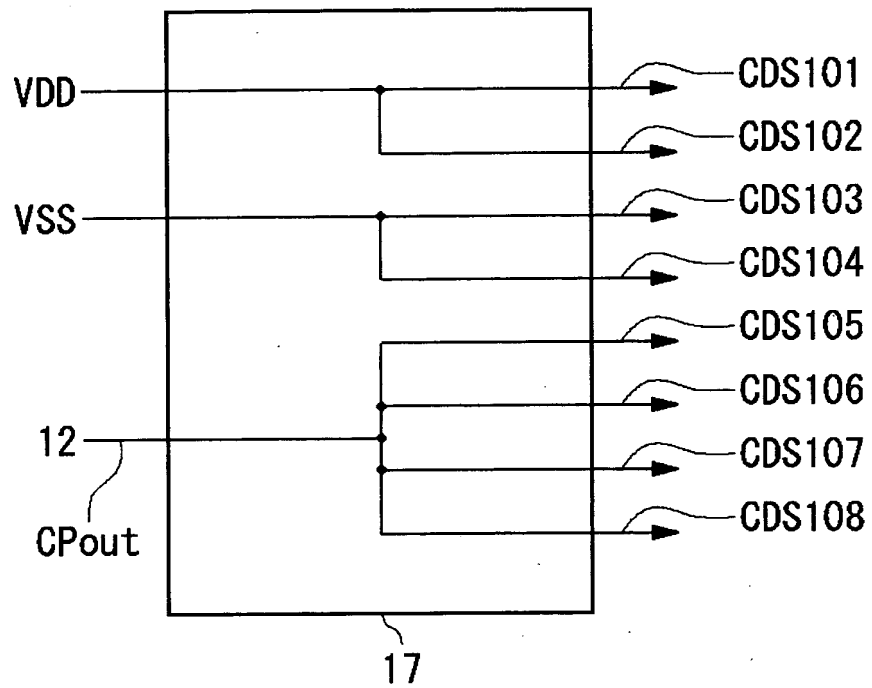


FIG. 5

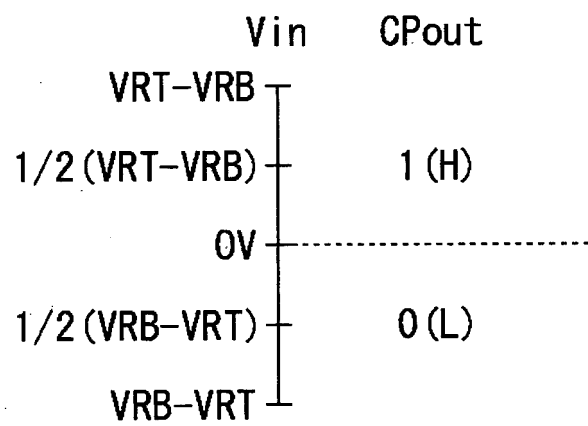


FIG. 6

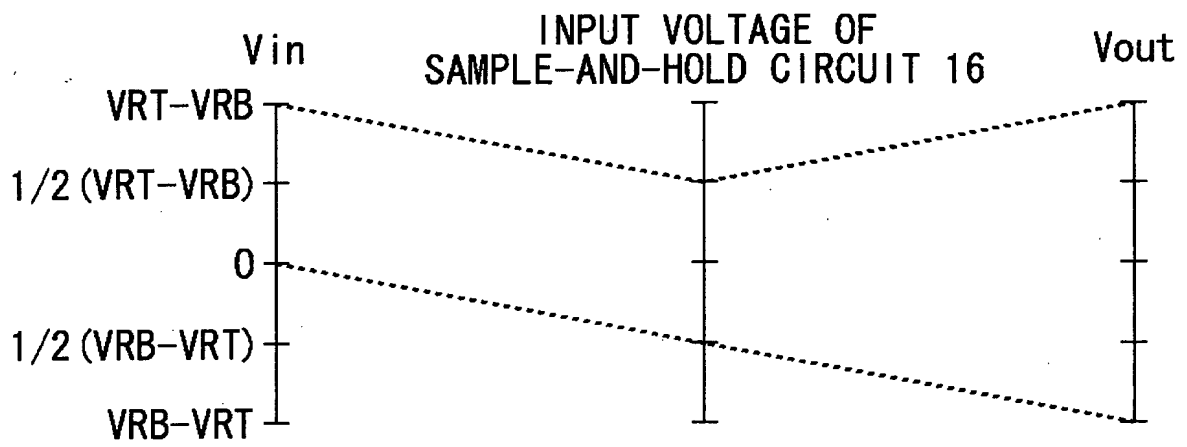


FIG. 7

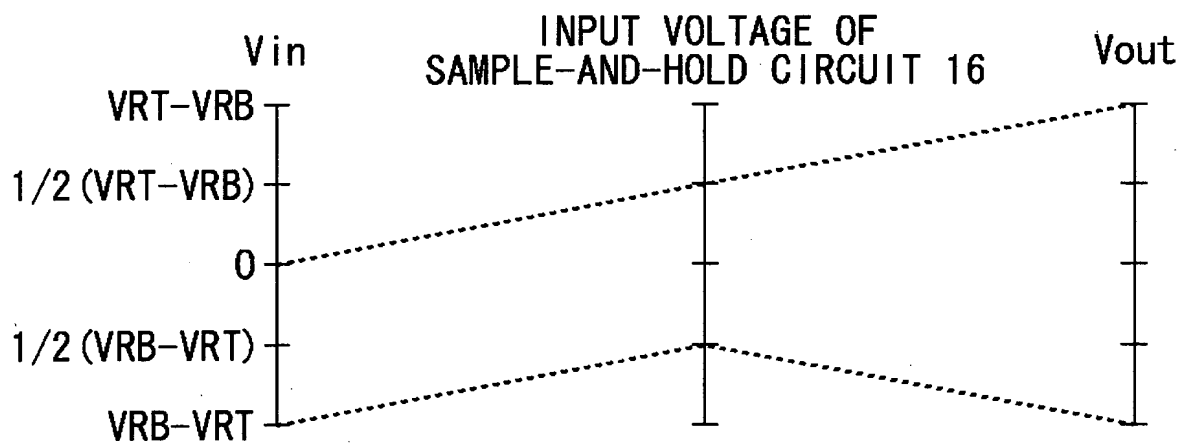


FIG. 8

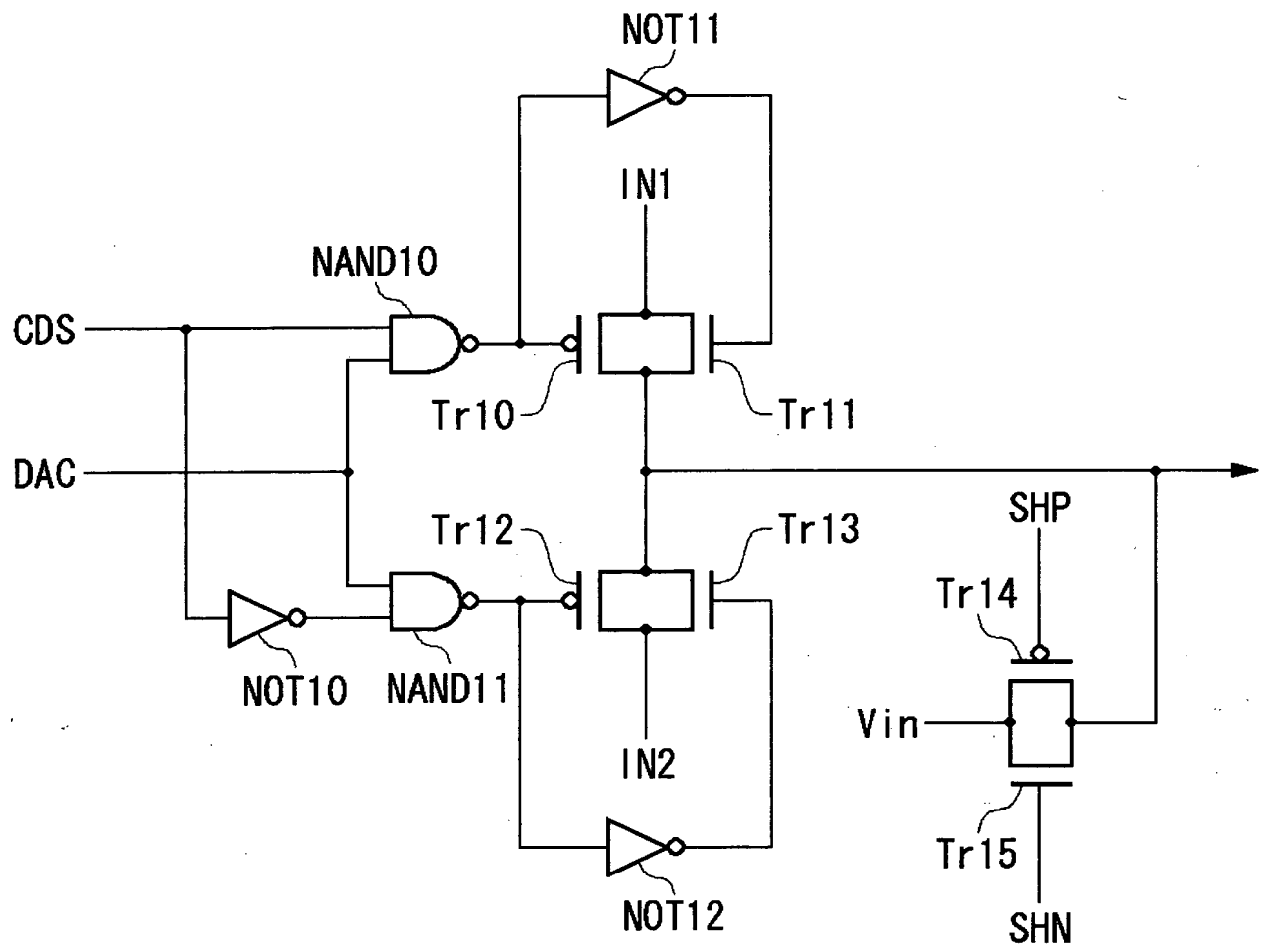


FIG. 9

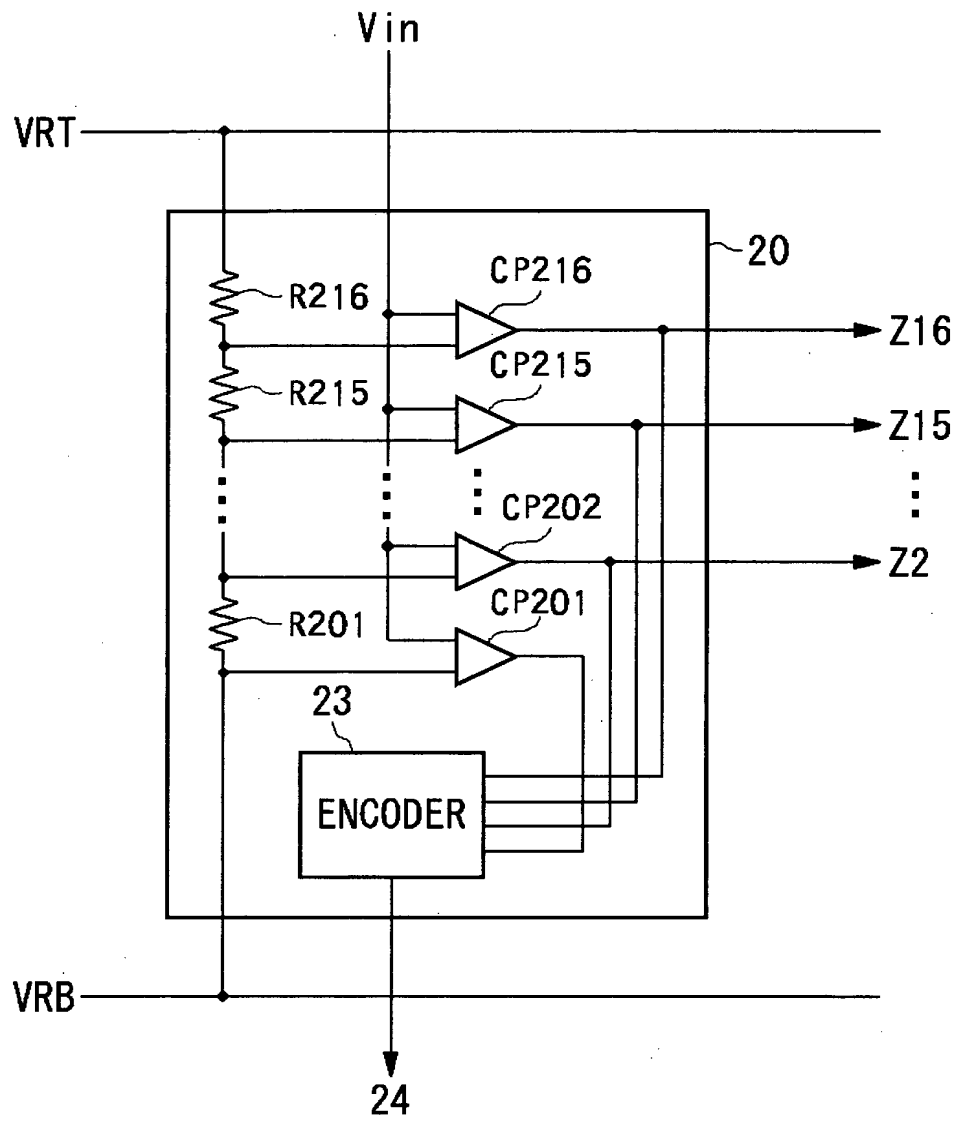


FIG. 10

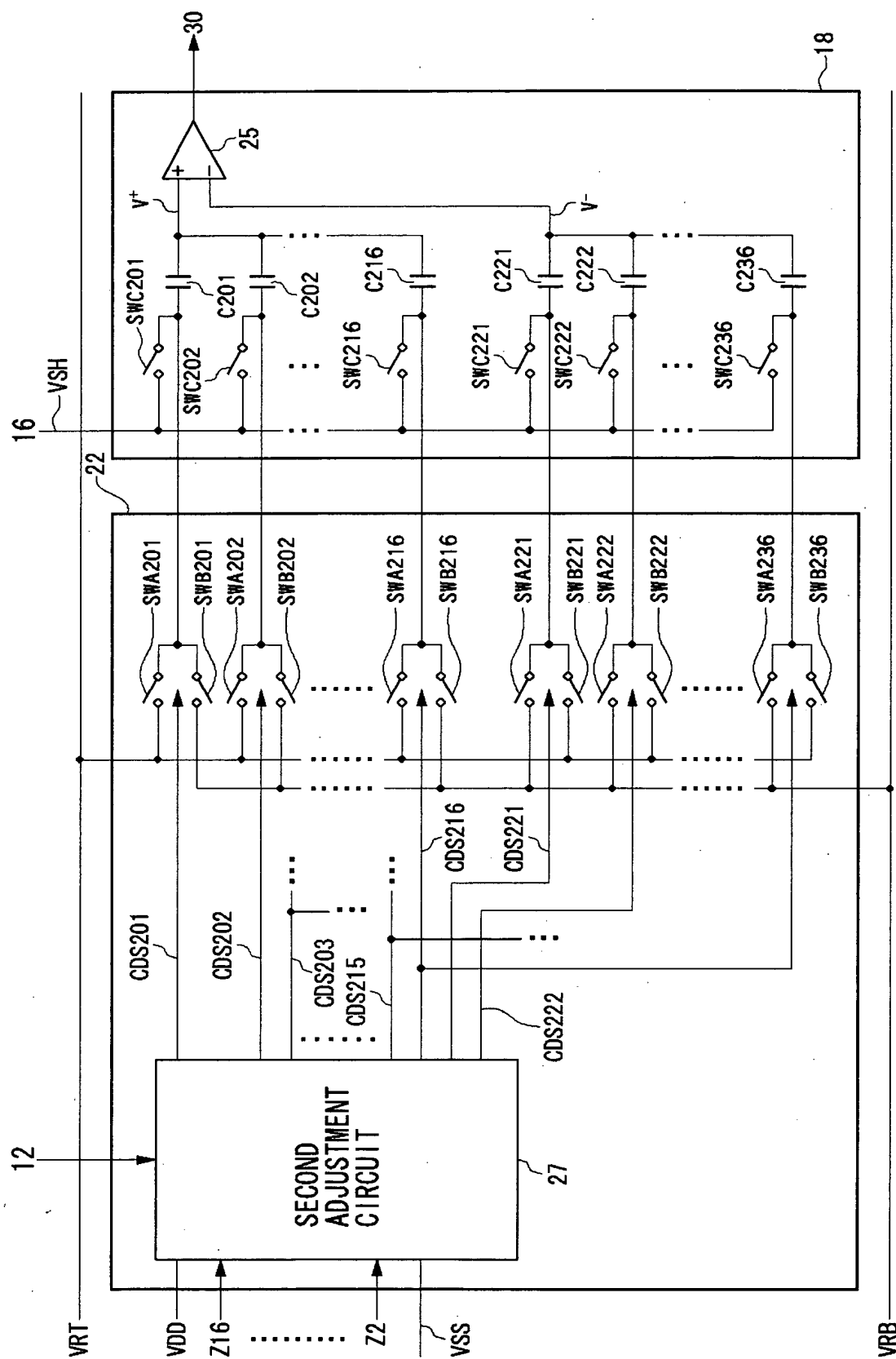


FIG. 11

	CPout=0	CPout=1
CDS201	VDD	VDD
CDS202	VDD	VDD
CDS203	Z2	Z10
CDS204	Z2	Z10
CDS205	Z3	Z11
CDS206	Z3	Z11
CDS207	Z4	Z12
CDS208	Z4	Z12
CDS209	Z5	Z13
CDS210	Z5	Z13
CDS211	Z6	Z14
CDS212	Z6	Z14
CDS213	Z7	Z15
CDS214	Z7	Z15
CDS215	Z8	Z16
CDS216	Z8	Z16
CDS221	VSS	VSS
CDS222	VSS	VSS

FIG. 12

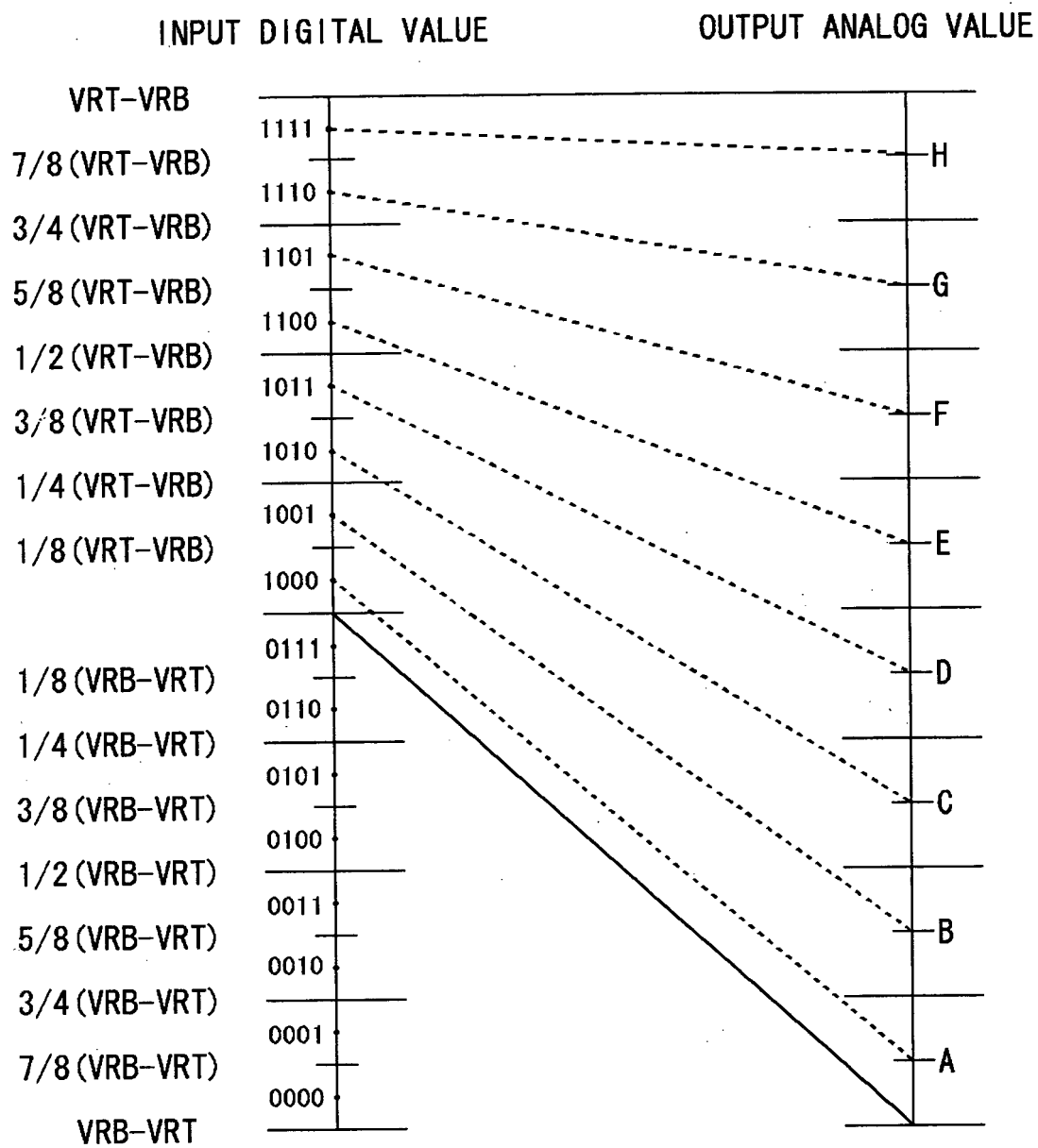


FIG. 13

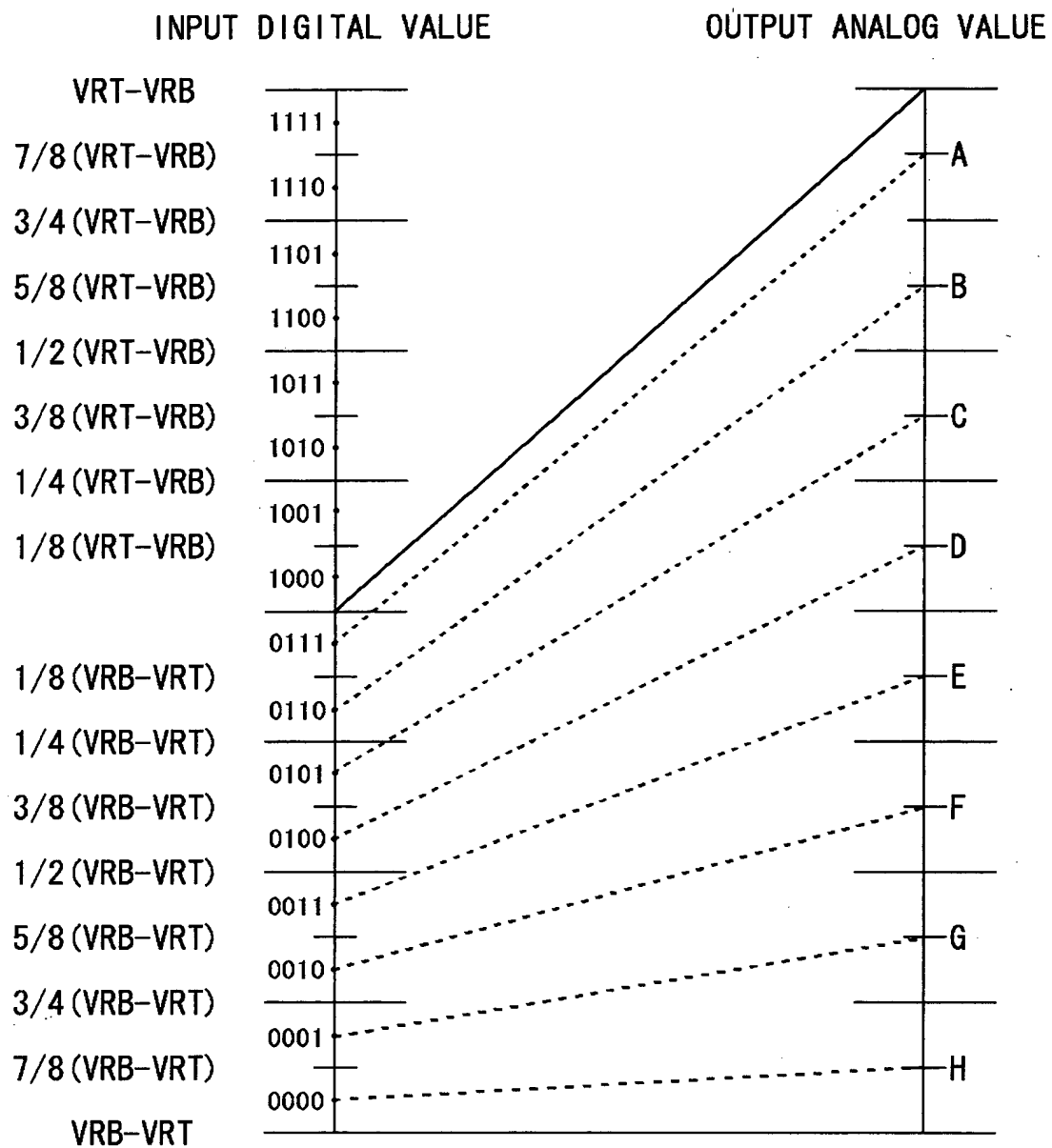
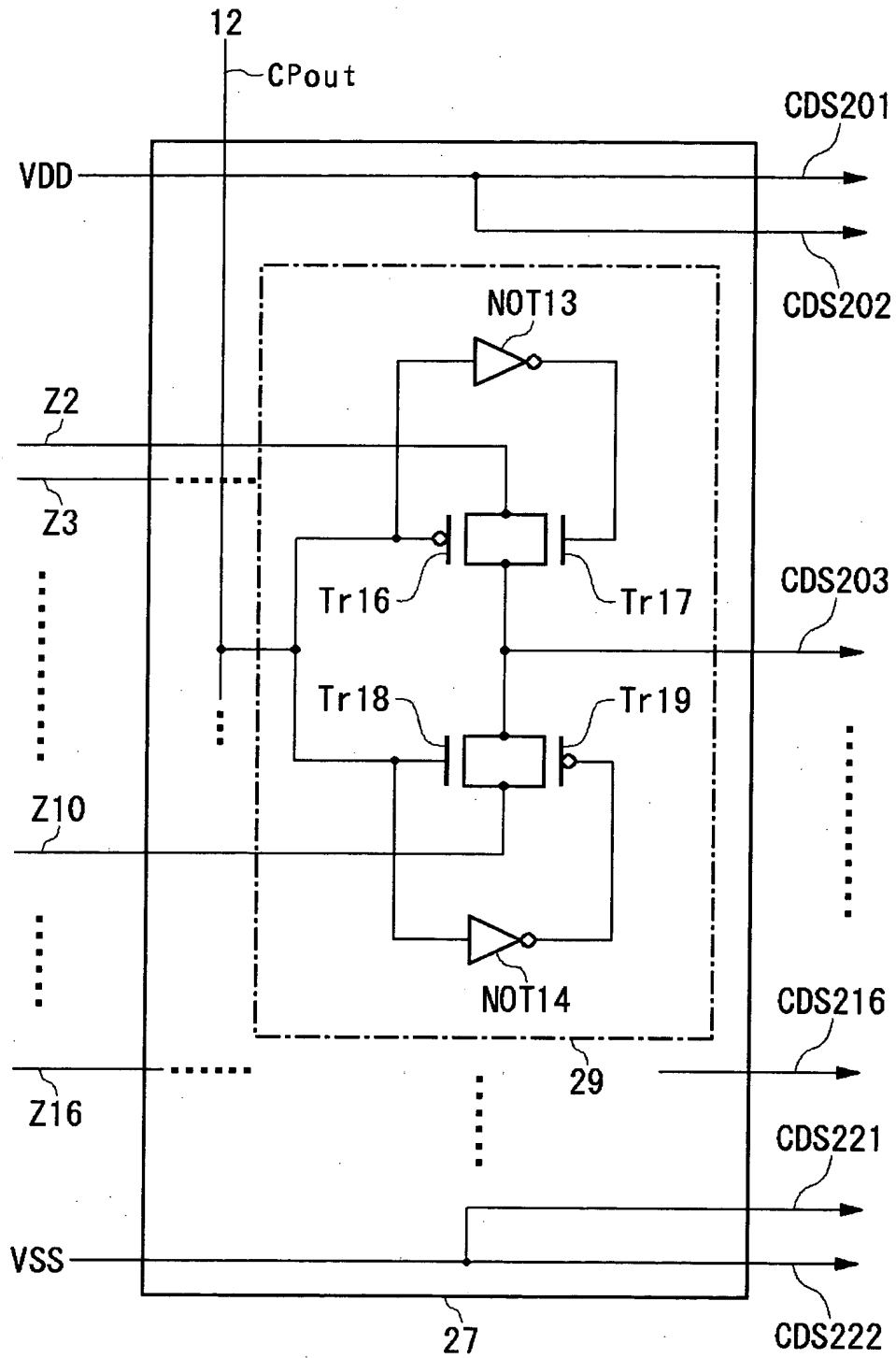


FIG. 14



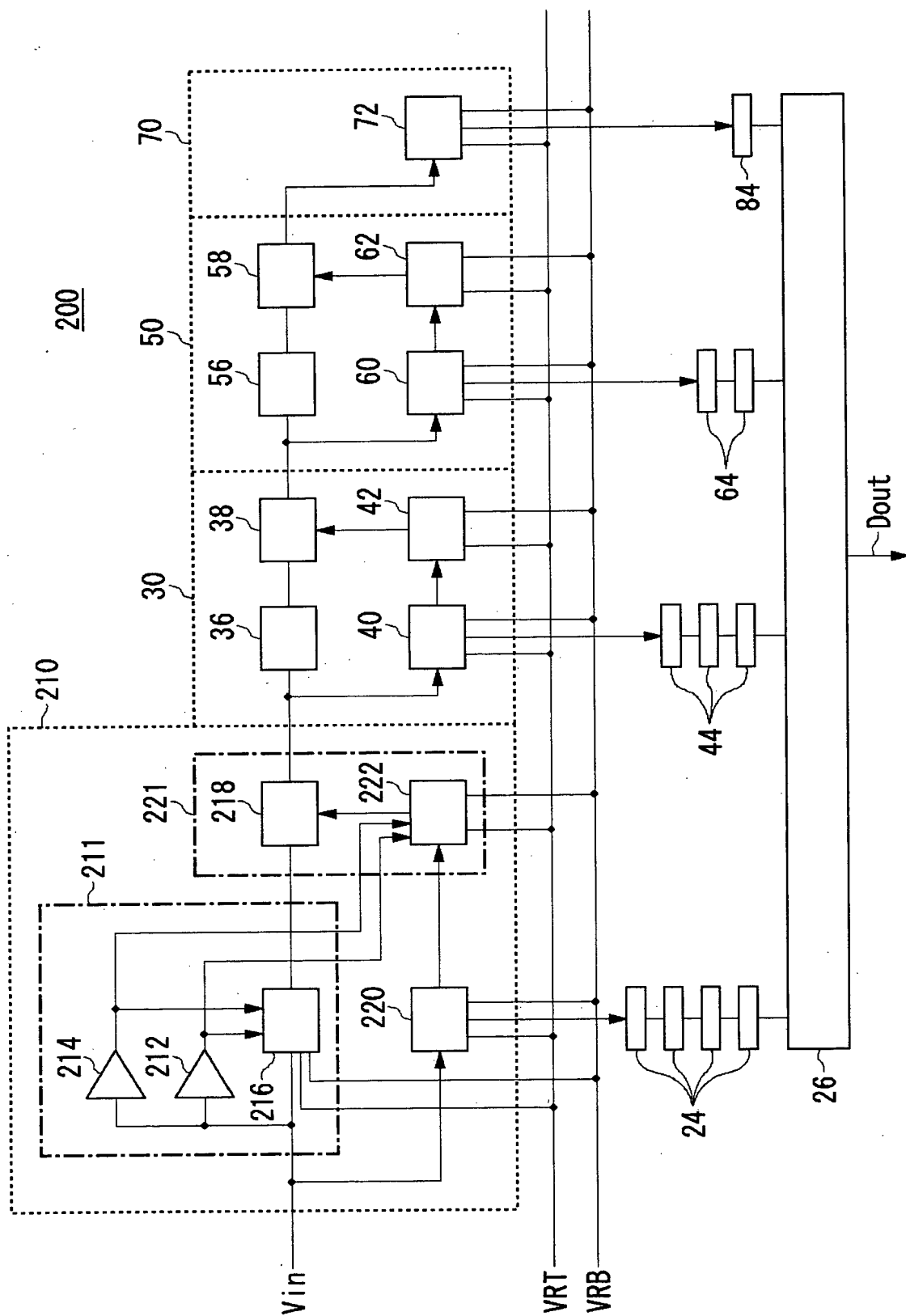


FIG. 16

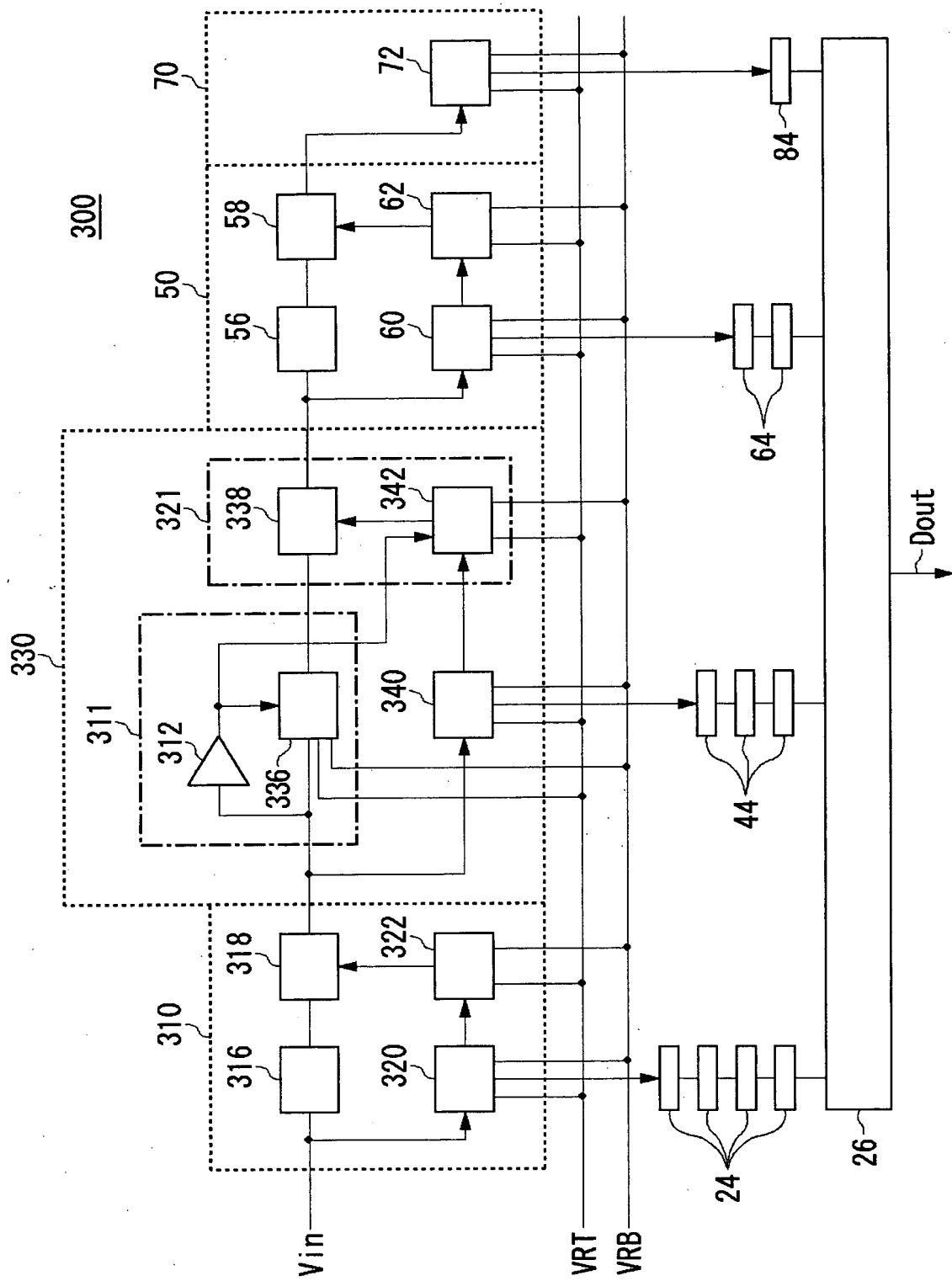


FIG. 17

